

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

U.S. OFFICE Patent Application	
M-8 O.I.P.E. SCANNED 1154 O.A. 1114	PATENT DATE

APPLICATION NO. 09/817107	CONT/PRIOR F	CLASS 257	SUBCLASS 48	ART UNIT <del>281</del> 5800	EXAMINER V. Rose
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## APPLICANTS

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**TITLE**

Integrated circuit provided with a substrate and with a memory, transponder, and method of programming a memory

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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